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MAINTENANCE HANDBOOK

FOR

JTM DIMMER RACKS



RANK STRAND ELECTRIC



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RANK AUDIO VISUAL LIMITED

SCOPE

This handbook contains information normally required for maintenance and servicing of the equipment. Commissioning and operational information, where relevant, is covered in the associated User's Handbook.

SERVICE ASSISTANCE

For assistance with servicing or maintenance, please contact the nearest Regional Office, Agent or Associate Company (see list attached at the end of this handbook) and state the Order Reference, Equipment Reference or other relevant information as well as an indication of the fault-symptom encountered.

Revision/amendment since last issue of document:

or on margin denotes minor changes.
against headings or illustration numbers denotes major revisions.



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CUIT DESCRIPTION

MAINTENANCE HANDBOOK

JTM 10/20-way DIMMER RACKS

ALR. MODULE

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SUPPLEMENTS

PCB-034: Service Aid Information on Printed-Circuit Boards.1-014: List of Regional Offices, Agents and Associate Companies.

ASSOCIATED PUBLICATIONS

05301-032: User's/Commissioning Handbood for JTM Dimmer Racks.

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1. CIRCUIT DESCRIPTION

See Wiring diagram on page 13.

1.1. DIMMER MODULE

1.1.1. PRINCIPLE OF OPERATION

See Fig. 1.1.1.

Inverse-parallel connected thyristors SCR1 and SCR2 are supplied with firing pulses from the Trigger Unit such that each thyristor is switched on at the same relative instant during the appropriate conductive half-cycle of mains supply. The value of control-signal input to the Trigger Unit determines the firing instant; since each thyristor, once switched on, conducts till the end of the half-cycle, the control signal also determines the conduction phaseangles of the SCRs and hence the averaged a-c output to the load. With zero control-signal input, the SCRs are triggered 'on' at or near the termination of their half-cycles supplies, giving zero or a required minimum a-c output to load; with maximum control-signal (negative d-c or half-wave rectified a-c), the SCRs are triggered 'on' near the start of respective half-cycles, giving maximum a-c output to load.



1.1.2. CIRCUIT DETAILS

Transformer T2 supplies 14V a-c to terminals 7 and 9 of the Trigger Unit. The control-signal input to Trigger Unit is applied via terminal D of the Dimmer Module, to terminal 3 of Trigger Unit, relative to terminal E of Dimmer Module and terminal 5 of Trigger Unit. This signal may be 24V a-c half-wave-rectified or a d-c signal, applied via a 10K ohms resistor.

Firing pulses to the SCRs are applied via terminal-pairs 11/12 and 14/16 of Trigger Unit.

The inductive Filter Unit serves to extend the rise-time of load output when the SCRs are switched 'on', and also to filter any switch-on transients, thus giving an output waveform suitable for interference-free operation of the lamps.

> NOTE: As shown in the Wiring Diagram on page 13, terminals E of all Modules were connected to the mains-earth stud (on Rack framework) in the earlier Racks. In present Racks, terminals E of all Modules and terminal C of the 'Control' terminal block are connected to an insulated 'Common-line' stud, and a link is provided between terminals C and E on the 'Control' terminal block. This link is removed when the control application requires isolation of common-line from the mains-earth.

1.2. TRIGGER UNIT: Ref: 932/14 or 934 (see para. 5.1.2)

NOTE: Ref. 932/14 Unit is designed for wired-in mounting and Ref. 934 Unit is designed for plug-in mounting on the Dimmer Module.

1.2.1. SUMMARY

An exponential saw-tooth wave-form, with a repetition rate of one per halfcycle of the mains-supply, is generated and compared with the input controlsignal, to switch 'on' a pulse-generator at the appropriate instant during each half-cycle; the 'ramp' wave-form and the pulse-generator are re-set at the termination of each half-cycle of mains-supply.

1.2.2. CIRCUIT DETAILS

See Fig. 1.2.2.1. for the circuit diagram; See Fig. 1.2.2.2. for nominal waveforms.

> NOTE: In the following description, unless otherwise stated, all voltages (and wave-form details) are nominal values and are referred to the common-line terminal 5.

Bridge-connected MR1 rectifies the 14V a-c supply fed to terminals 7 and 9 (waveform 1) to give a series of negative-half-cycle output (waveform 2) at R1-MR2 junction. MR2 isolates C6 from the 'positive' peaks of the above output and allows C6 to be charged negative to about -16V. R9 and MR4

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provide stabilised -12V supply, by-passed by C3, to terminal 1 and the circuits of VT2-to-VT5.

VT1 is switched off for a 500uS period coincident with the 'positive' peaks of MR1 output (i.e. coincident with the zero-crossover instants of a a-c mainssupply), and is switched on for the rest of each half-cycle. With VT1 off, VT2 is switched on, discharging C2 and switching VT4 on; this in turn switches VT5 on and VT6 off. With VT1 on, VT2 is switched off; since C2 had been previously discharged, VT4 remains on, and capacitor C2 is charged negative via R5 and the base-emitter junction of VT4 (wave-form 4).

The input control-signal to terminal 3 is smoothed by C4, potential-divided by R8-VR2 and applied to VT3 base. VR1 provides gain-controlling feedback to d-c amplifier VT3. For given settings of VR1 and VR2, the value of control-signal input determines the collector-current of VT3 and hence the potential, due to this current alone, at VT3 collector and VT4 emitter. C2 has to charge to approximately this potential before VT4 is switched off, in turn switching VT5 off, and switching on the blocking oscillator circuit of VT6 to generate a 100uS pulse-train (waveform 5) which is fed, via the secondaries of T1, to the associated thyristors.

Thus, with zero signal input to terminal 3, the low collector current of VT3 requires C2 to charge to a higher voltage to switch VT4 off; hence 'firing' occurs near the end of each half-cycle. With a negative signal input, the higher collector current of VT3 requires C2 to charge to a lower voltage to switch VT4 off; hence the 'firing' instant is time-advanced in each half-cycle.

When VT6 is switched off coincident with the zero crossover instants of mains-supply, the pulses stop. Diode MR6 limits the back-emf in Tl primary to a safe value when VT6 is switched off.

1.2.3. BOTTOM AND TOP SET ADJUSTMENTS

VR1 (BOTTOM SET) is normally set to ensure that, with zero input, VT4 does not turn off till near the end of each half-cycle; VR2 (TOP SET) is set with maximum (negative) signal input, to give maximum output from the associated thyristors.

2. CHECK AND TEST PROCEDURE:

NOTE: Use a voltmeter with sensitivity of not less than 10K ohm/V for all voltage measurements other than load output voltages; see NOTE in para. 4 below; see para. 4.1 of User's Handbook for output measurements.

A double-beam oscilloscope is advisable for waveform-checks since this enables comparison with mains-supply waveform.



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2.1. RACKS

Check that the a-c supply to the Rack is switched on; note that the neon on the fuse-panel or circuit-breaker panel indicates presence of a-c supply to the fan only; failure of the fan fuse will switch off this neon to give 'fan-off' indication; note also that, with the above panel hinged open, the fuse-holder or circuit-breaker terminals are 'live' with a-c supply to rack switched on.

Check that the force-vent fan is operating satisfactorily; the fan supply is separately fused.

The 'Control' fuse or circuit-breaker protects the a-c supply to the associated Desk. The 10 (or 20) other fuses/circuit-breakers are numbered to correspond with the Dimmer Modules whose a-c supply and load circuits are thus protected.

2.2. DIMMER MODULES

NOTE: All voltages, unless otherwise stated, are nominal values referred to common-terminal E on the Module and terminal 5 on Trigger-card. Note also that the thyristor heat-sinks are "live".

See Figures 2.2 and 2.3 in the User's Handbook 05301-032.

Check the primary input voltage (200-250V) and secondary output (14V) of transformer T2.

Check input voltage to terminal D (relative to E) and output voltage to load.

2.3. TRIGGER CARDS

Check the d-c voltages across C6 (-16V \pm 10%), and C3 (-12V \pm 10%).

Check that the input voltage at terminal 3 varies from zero to a negative value (of the order of 7V or so) as the associated channel-lever, and master-fader if any, are operated.

Check the following waveforms (see Fig. 1.2.2.2.):

Switching waveform at VT1 collector; 'Ramp' waveform at VT2 collector (across C2); Pulse waveform at VT5 collector.

Check setting up of VR1 and VR2 on Trigger Unit as detailed in para. 4.1 of the User's Handbook 05301-032.

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Module		Thyristor			
Ref. No.	V & KW Rating	Make (Note 1)	Type No.	Tightening Torque	
		dise Rack.		lb-in	Nm
JTM10 or JTM10C	240V, 1KW	W-H W-H AEI IRC	CS22M/CF U1211/B5 RS 1/5 16RC60	30-35 42 30-35 20-25	3.37-3.93 4.72 3.37-3.93 2.24-2.8
JTM20 or JTM20C	240V, 2KW	W-H W-H AEI IRC	CS21M/CF U1210/B5 RS9/5 30RCS60	30-35 42 30-35 25-30	3.37-3.93 4.72 3.37-3.93 2.8-3.37
JTM20L or JTM20LC	120V, 2KW	W-H AEI AEL AEI AEI AEI IRC	U1108/3 CR30201A CR30203A CR30303A RS5/3 RS9/3 40RCS30	42 85-100 85-100 85-100 30-35 30-35 25-30	4.72 9.64-11.23 9.64-11.23 9.64-11.23 3.37-3.93 3.37-3.93 2.8-3.37
JTM30L or JTM30LC	120V, 3KW	W-H AEI AEI AEI AEI IRC	U1108/3 CR30201A CR30203A CR30303A RS5/3 40RCS30	42 85-100 85-100 85-100 30-35 25-30	4.72 9.64-11.23 9.64-11.23 9.64-11.23 3.37-3.93 2.8-3.37
JT M50C	240V, 5KW	W-H , W-H AEI AEI AEI IRC	CS31MC/F U1108/5 CR30403A CR30503A RS3/6 40RCS60	40-50 42 85-100 85-100 85 25-30	4.5-5.6 4.72 9.64-11.23 9.64-11.23 9.64 2.8-3.37
JTM60L	120V, 6KW	W-H W-H AEI IRC	42T3 U1109/3 RS7/4 71RA30	84* 126 85-100 120-160	94.3 141 9.64-11.23 134-180

IRC = International Rectifier

* : on each base-mounting bolt

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3. DISMANTLING/REPLACEMENT INFORMATION

3.1. DIMMER MODULES

In early versions of Dimmer Racks, the Modules were secured to the rack by four screws through the base-plate. In the present Racks, the Modules are secured by nuts on to studs provided in the Rack.

3.2. THYRISTORS

Do not attempt to remove the heat-sinks from the module base-plate; unsolder or disconnect the thyristor gate and cathode connections, and remove the thyristor from its heat-sink.

When replacing a thyristor: (a) use a replacement of the same type as the one removed from the module; note that alternative types listed for certain modules in the table below are not direct replacements but are electrical alternatives only, especially when the manufacturers differ; (b) ensure that the correct tightening torque is applied on the nut or nuts securing the thyristor to its heat-sink (see Table 3.2. on Page 7).

3.3. TRIGGER UNIT - PRINTED-CIRCUIT BOARDS

Where these are wired-in, they are secured to the top of the thyristor heatsinks by four nylon studs.

4. MAINTENANCE INFORMATION

NOTE: Only a qualified electrician familiar with the equipment should undertake replacement or other maintenance work in the racks.

Always switch off the rack-supply isolator before undertaking any maintenance work on its interior. Remember that high voltages are present at thyristor heat-sinks and associated wiring, and take suitable precautions when testing or measuring with the supply switched on.

4.1. ROUTINE MAINTENANCE

The racks themselves require little routine maintenance apart from some attention to the fan (see paragraph 4.1.1. below) and periodical inspection of associated wiring and connections and, in very dust-prone environments, inspection of the rack-interior for any accumulation of dust; use a soft brush and suction-type low-pressure air-cleaner to effect the necessary cleaning.

4.1.1. FANS

The fan manufacturers advise that MK 3B and MK 4B - 6" - 1400 rpm fans have sealed lubrication for the bearings and this is normally adequate for 10,000 hrs. operating time; contact the Maintenance Department of Rank Strand Electric Limited for bearing replacement at the end of this period.

4.2. FAULT DIAGNOSIS

4.2.1. RACKS

Failure of a-c supply to associated Desk: Check 'Control' fuse or circuitbreaker.

Failure of force-vent fan and the neon indicator: Check the fuse above the 'Fan' label.

Complete failure of output to load <u>and</u> of a-c supply to the associated Module: Check the numbered fuse.

Failure of output to load only: Replace the Dimmer module as a complete unit and set up VR1 and VR2 as in para. 4.1 of the User's Handbook. If the module has a plug-in Trigger-card, replace this card first, to check if this is the faulty unit. For detailed servicing, where it is convenient to do so, the procedure given in para. 4.2.1.2. may be helpful.

4.2.1.1. Check presence of 'firing' pulses at VT 6 collector on the Triggercard (see Fig. 1.2.2.2.) with an oscilloscope; if this is satisfactory, check the load and thyristor circuits - see below; if the pulse-check is not satisfactory, replace the trigger-card unit or check it in detail as in para 2.3.

4.2.1.2. Insufficient output to load: Confirm, by means of the test above, that the Trigger-card is satisfactory. Check waveform of output voltage across load, comparing it with the mains-supply waveform if possible - see Fig. 1.2.2.2.

If alternate half-cycles of output are missing (see Fig. 4.2.A), this indicates failure of one thyristor; check a-c supply to anode, and continuity from cathode to load; replace thyristor if the above two sets are OK; see para 3.2.

FIG.4.2B

If alternate half-cycles of output waveform are complete half-cycles (see Fig. 4.2.B), this indicates a short-circuit across (or between) the anode and cathode of the respective thyristor; replace the thyristor.

4.2.2. TRIGGER-CARD UNITS

Specially where these units are plug-in items and also on wired in items, the following check may help to isolate a faulty unit:

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Check 14V a-c supply to terminals 7 and 9.

Check that the control voltage at terminal 3 can be varied from zero (or other minimum value) to -15V (or other maximum value, as set-up on site); if these tests are satisfactory, check presence of "firing" pulses at VT5 collector (the transistor-case) and that the timing of these pulses is controlled by the input voltage as shown in Fig. 1.2.2.2. If this test is not satisfactory, replace the Trigger card and set VR1 and VR2 as in para. 4.1. of the User's Handbook.

4.2 2.1. DETAILED TESTS ON TRIGGER CARD See para. 2.3.

4.2 3. TRANSFORMER-FED LOADS

Figure 4.2.3. illustrates the typical external connection to L and N terminals of a Dimmer Module when used, for example, with a step-down transformer and a low-voltage lamp. Open-circuit of the fixed load across the transformer primary in such applications may result in overheating of, and damage to, the transformer <u>if simultaneously the secondary load or circuit also becomes opencircuit</u>; this is due to half-waving and the resultant large d-c component of currents in the transformer.



5. PARTS LIST INFORMATION

5.1 COMPLETE ASSEMBLIES AND SUB-ASSEMBLIES

5.1.1. DIMMER MODULES

Rating

Ref. No.

lKW,	200-250V	JTM10 or 10C
2KW,	200-250V	JTM20 or 20C
2KW,	110-120V	JTM20L or 20LC
3KW,	110-120V	JTM30L or 30LC
5KW,	200-250V	JTM50C
6KW,	110-120V	JTM60L with filter separate.

5.1.2. TRIGGER-UNIT PRINTED-CIRCUIT ASSEMBLY

Plug-in type:	Ref. No.	934 or	934/2
Wired-in type:	Ref. No.	932/14	or 932/2

5.2. COMPONENTS about 05 about 05 M TL aC

5.2.1. RACK COMPONENTS DE LOCM COMPONENTS

Fan: A03-2018	240V 50Hz :	MK3B Plannette, 6", rpm, with cowl	1400
	110V 60Hz :	MK4B Plannette, 6", rpm, with cowl.	1400
Neon Indicators	220V : or	Thorn SGF 16/220 Ye Thorn SGF 16/110 Ye	llow
Fan Fuse:	Belling-Lee L10	055, 5A	
'Control' Fuse:	Reyrolle F190 (or Circuit-breaker racks).	on 200-240V racks); , Heinemann* 10A (on	110-120V
Dimmer/Load protection:	JTM10 or 10C: JTM20 or 20C: JTM20L or 20L0 JTM30L or 30L0	Fuse, Reyrolle F190, Fuse, Reyrolle F562, C: Circuit-breaker, H C: Circuit-breaker, H	No. LA5. No. LC10. Ieinemann* Ieinemann*
	JTM60L:	Circuit-breaker, H	leinemann*

5.2.2. DIMMER MODULE COMPONENTS

(Trigger Unit:	see para. 5.1.2)	
Transformer T2:	TRX981 on 110-120V Modules TRX976 on 200-250V Modules	אד אק ; אוס ;
Capacitor Cl: 00000	0.5uF 250V a-c, Hunts type L List No. T25511 - on 110-120 0.1uF 300V a-c, Hunts AB112 200-250V Modules	45/6, V Modules; - on
Thyristors SCR1 and SCR2:	see para. 3.2	
Filter Units	On JTM10 & JTM20 Modules: On JTM10C Modules: On JTM20C Modules: On JTM20L Modules: On JTM20LC & 30LC Modules: On JTM30L Modules: On JTM50C Modules: For JTM60L Modules:	J2HV50 J1HV50C J5HV50CX J2LV60 J3LV60C J3LV60 J5HV50CX J6LV60
	(lan	ninated core); J6LV60C (C-core)

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Fuses on Low-	On JTM20L & 20LC Modules:	
voltage Modules:	English Electric, HRC,	C25K-25A
	On JTM30L & 30LC Modules:	
	English Electric, HRC,	C30K-30A
	On JTM60L Modules:	
	English Electric, HRC,	C60K-60A

5.2.3. TRIGGER-CARD COMPONENTS

(see Fig. 1.2.2.1)

Circuit	Description	Part or Ref. No.
C2 C3 C4 C5 C6	Capacitor, 0. luF 250V, 25uf 25V, 2.5uF 64V, 0.33uF 250V 250uF 25V	Mullard C281AB/A100K Mullard C426AR/F25 Mullard C426AR/H2.5 Mullard C281AB/A330K Mullard C437AR/F250
MR1 MR2, MR5 MR4	Rectifier, Westinghouse Diode, Texas Instrumen Diode, Zener, Texas Ins AEI: MR120-	, RA12022/2 Bridge ts, 1S920 struments:1S2120A; H
R1	Resistor, 1K 10 $\% \frac{1}{4}$ W, E	rie type 16 or Morganite Type S
R2, R8 R4 R5 R6, R13 R7 R9 R10 R11, 12	10K (as above 100K (as abov 5.6K (as abov 100 ohms (as 2.7K (as abov 330 ohms (as 3.9K (as abov 10 ohms (as a) e) above) e) above) e) bove)
T1	Pulse transformer TRX	\$770
VR1, VR2	Potentiometer, 50K, Mo	rganite 62H
VT1-to-VT3) & VT5) VT4 VT6	Transistor, ME 0404/2 (ME 6002 (M NKT 228/S1	(Micro Electronics) icro Electronics) Newmarket

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